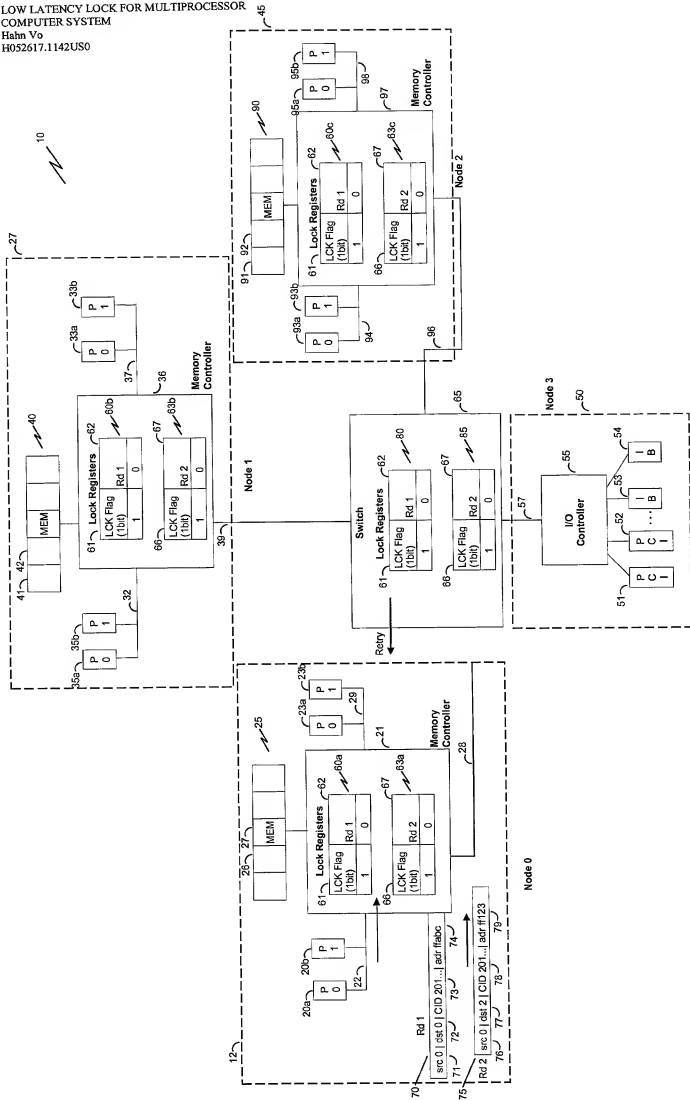
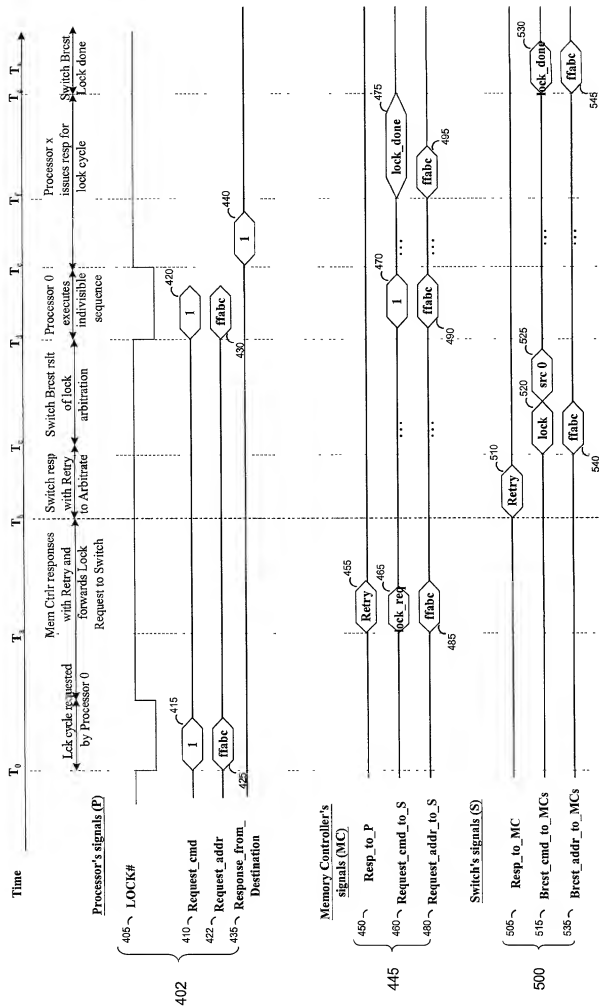
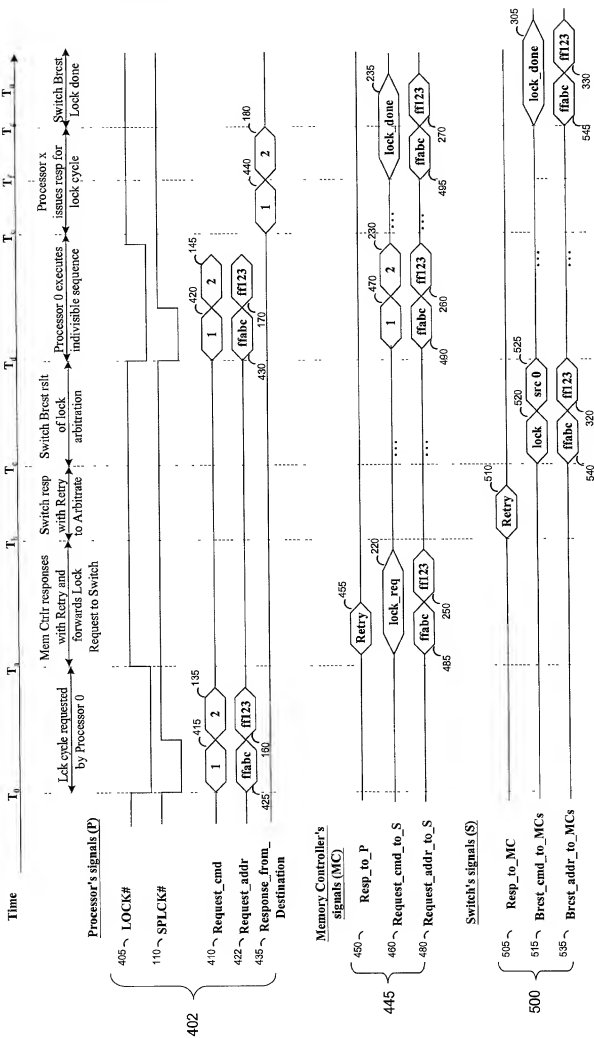


FIGURE 1

FIGURE 1







LOCK FLOW CHART

FIGURE 4

TITLE: LOW LATENCY LOCK FOR MULTIPROCESSOR
COMPUTER SYSTEM
INVENTOR: Hahn Vo
ATTY DKT NO.: H052617.1142US0

FIGURE 4

